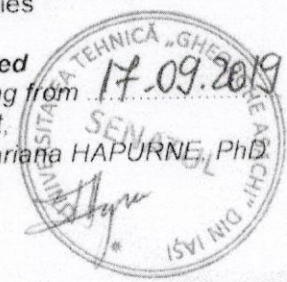


*Approved*  
 In Senat meeting from 17.09.2019  
 Senat President,  
 Assoc. Prof. Tania Mariana HAPURNE, PhD



**CURRICULUM**

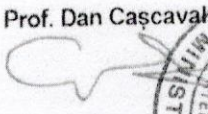
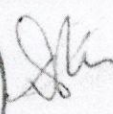

**1<sup>st</sup> Year 2019 - 2020**

No.	Name of the discipline	Disc. Code ETTI	Condi-tional-ities	1st Semester						2nd Semester							
				No. hrs/week/discipline					Fin Eval	K	No. hrs/week/discipline					Fin Eval	K
				C	S	L	P	IW			C	S	L	P	IW		
101	Mathematical Analysis	EDIF101		2	2	-	-	64	E	5							
102	Linear Algebra, Analytical and Differential Geometry	EDIF102		2	3	-	-	50	E	5							
103	Fundamentals of Electrical Engineering 1	EDID131		2		2		50	VP	5							
104	Physics 1	EDIF132		2	1	1	-	16	E	3							
105	Computer Programming and Programming Languages 1	EDIF104		2	-	2	-	64	E	5							
106	Foreign Language 1	EDIC107		-	1	-	-	34	C	2							
107	Sports 1	EDIC108		-	-	1	-	10	A/R	1							
MD	108	Special Mathematics 1	EDIF133								3	3	-	-	36	E	5
	109	Fundamentals of Electrical Engineering 2	EDID134								2	1	-	-	44	E	3
	110	Physics 2	EDIF139								1	-	1	-	44	VP	3
	111	Computer Programming and Programming Languages 2	EDIF113								2	-	2	-	40	C	4
	112	Computer-Aided Graphics	EDIF105								1	-	2	-	30	E	3
	113	Passive Devices and Circuits	EDID106								2	-	2	-	64	E	5
	114	Foreign Language 2	EDIC114								1	1	-	-	20	C	2
	115	Sports 2	EDIC115								-	-	1	-	10	A/R	1
116	1. Applied Informatics 1	EDOF135		2	-	1	-	54	VP	4							
	2. Numerical methods	EDOF136		2	1	-	-										
	1. Applied Informatics 2	EDOF137									1	-	-	2	54	VP	4
	2. Probabilities and Statistics	EDOF138									2	1	-	-			
FD	118	Introduction to Analog Circuits	EDLS120								1	-	2	-	30	C	3
	119	Applied Mathematical Complements	EDLC121								2	2	-	-	40	C	4
	120	Romanian Language 1	EDLC117		-	2	-	-	20	C	2						
	121	Romanian Language 2	EDLC118								-	2	-	-	20	C	2
	Total hours per week, total assessments per semester and total credits per semester for mandatory discipline (MD) and elective discipline (ED)				12	9	6	-	342	4E 2VP 1C 1A/R	30	13	5	8	2	342	4E 2VP 2C 1A/R
					10	5	-					14	6	0			
					27							28					

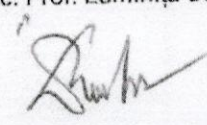
MD – mandatory discipline; ED –elective discipline; FD –facultative discipline ; IA – independent activities; C – complementary; D- from the domain, F – fundamental, S – specialization; V- verification; K- credits; E- Exam, C- colloquium; A/R – accept/reject; IW - individual work; \* = credits and verifications not included in the total T - Telecommunication Technologies and Systems, M – Microelectronics, Optoelectronics and Nanotechnologies, E – Applied Electronics.  
 Discipline code: ETTI + 3 letters + 3 figures + a, where a = missing / letter = common for the faculty/section .

**RECTOR**  
 Prof. Dan Cașcaval, PhD

**DEAN**  
 Prof. Daniela Târniceriu, PhD.

**HEAD OF DEPARTMENT**  
 Assoc. Prof. Luminița Scripcariu, PhD.



TECHNICAL UNIVERSITY "GHEORGHE ASACHI" IAȘI  
 FACULTY OF ELECTRONICS, TELECOMMUNICATIONS AND INFORMATION TECHNOLOGY  
 Profile: Electronic Engineering, Telecommunications and Information Technologies  
 Specialization: Telecommunication Technologies and Systems  
 Language: English  
 Academic degree: B.S. (engineer)  
 Duration of study: 4 years  
 Degree Program: full time

Approved 28.09.2018  
 In Senat meeting from .....  
 Senat President,  
 Assoc. Prof. Tania Mariana HAPURNE, PhD.



CURRICULUM

2nd Year 2019- 2020

No.	Name of the discipline	Disc. Code ETTI_	Condi- tionali- ties	1st Semester *							2nd Semester								
				No. hrs/week/discipline					Fin Eval	K	No. hrs/week/discipline					Fin Eval	K		
				C	S	L	P	IW			C	S	L	P	IW				
201	Electronic Devices	EDID201		4	1	2	-	70	E	7									
202	Signals, Circuits and Systems	EDID202		4	-	3	-	70	E	7									
203	Computer-Aided Analysis of Electronic Circuits	EDIS203		2	-	2	-	88	E	6									
204	Special Mathematics 2	EDIF204		3	2	-	-	98	E	7									
205	Foreign Language 3	EDIC206		1	1	-	-	20	C	2									
206	Sports 3	EDIC207		-	-	1	-	10	A/R	1									
MD	207	Electronic and Telecommunications Measurements	EDID205								2	-	1	-	30	C	3		
	208	Information Theory	EDID208								3	-	2	-	26	E	4		
	209	Fundamental Electronic Circuits	EDID209								3	-	2	1	60	E	6		
	210	Signals and Systems	EDID210								3	-	3	-	60	E	6		
	211	Digital Integrated Circuits	EDID211								3	-	2	1	60	E	6		
	212	Foreign Language 4	EDIC 212								-	1	-	-	10	C	1		
IA	213	Training	EDID213								3 x 30h/w =90h					A/R	4		
FD	214	WEB Technologies	EDLS214		2	-	2	-	40	E	4								
	215	Introduction to Logic Circuits	EDLC215		1	-	1	-	68	C	4								
	216	Sports 4	EDIC216									-	-	2	-	20	A/R	2	
	217	Romanian Language 3	EDLC217		2	-	2	-	40	E	4								
	218	Ethics and Integrity	EDLC218									2	1	-	-	54	V	4	
	219	Romanian Language 4	EDLC218									-	2	-	-	20	C	2	
Total hours per week, total assessments per semester and total credits per semester for mandatory discipline (MD) and elective discipline (ED)				14	4	8	0	356	4E	1C	30	14	1	10	2	246	4E	2C	30
				26							27								

MD – mandatory discipline; ED –elective discipline; FD –facultative discipline ; IA – independent activities; C – complementary; D- from the domain, F – fundamental, S – specialization; V- verification; K- credits; E- Exam; C- colloquium; A/R – accept/reject, IW - individual work; \* = credits and verifications not included in the total. T – Telecommunication Technologies and Systems, M – Microelectronics, Optoelectronics and Nanotechnologies, E – Applied Electronics.  
 Discipline code: ETTI + 3 letters + 3 figures +α, where α = missing / letter = common for the faculty/section .

RECTOR  
 Prof. Dan Cașcaval, PhD.

DEAN  
 Prof. Daniela Tărniceriu, PhD.

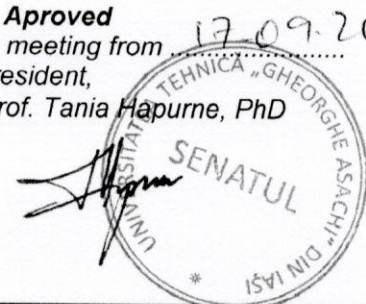
HEAD OF DEPARTMENT  
 Assoc. Prof. Luminița Scripcariu, PhD.



*[Handwritten signature]*

*[Handwritten signature]*

**Approved**  
 In Senat meeting from 17.09.2019.  
 Senat President,  
 Assoc.Prof. Tania Hapurne, PhD



**CURRICULUM**

**3rd Year 2019 - 2020**

No.	Name of the discipline	Disc. Code ETTI__	Con- di- tion- ali- ties	1st Semester							2nd Semester									
				No. hrs/week./discipline						Fin Eval	K	No. hrs/week./discipline					Fin Eval	K		
				C	S	L	P	SI	SI			C	S	L	P	SI				
ND	301	Analog Integrated Circuits	EDID301	3	-	2	1	98	E	7										
	302	Introduction to Communications	EDID302	3	-	1	-	74	C	5										
	303	Television	EDID303	3	-	1	-	48	C	4										
	304	Microcontrollers	EDIS304T	2	-	2	-	48	E	4										
	305	Digital Signal Processing	EDID305ET	3	-	2	-	86	E	6										
	306	Decision and Estimation in Data Processing	EDID306T	2	1	1	-	48	E	4										
	307	Computer Networks and Operating Systems	EDID307T									3	-	2	-	60	C	5		
	308	Electronic Equipments for Measuring and Control	EDIS308									3	-	1	1	60	E	5		
	309	Communication Systems	EDID309T									3	-	2	-	60	E	5		
	310	Computer - Aided Design of Analog and Digital Systems	EDID310T									2	-	2	1	60	E	5		
	311	Power Electronics	EDID311T									2	-	1	-	36	C	3		
ED	312	1. Antennas and Propagation	EDOS301T								2	-	1	-	62	E	4			
		2. Fundamentals of Radiocommunications	EDOS302T																	
IA	313	Training	EDID313								3s x 30ore/s=90 ore					-	A/R	3		
FD	314	Microcontroller Applications	EDLS314	-	-	-	2	24	C	2										
	315	Sports 5	EDLC315	-	-	2	-	24	A/R	2										
	316	Sports 6	EDLC316											2	-	24	A/R	2		
	317	Technological Fundamentals of Microelectronics	EDIS309M								2	-	2	-	74	E	5			
	318	Analog Circuits Synthesis	EDIS310M								2	-	2	-	48	E	4			
	319	Medical Electronics and Informatics	EDOS315E								3	-	2	-	60	E	5			
	320	Integrated Structures for Specific Applications 1	EDLS318M								2	-	2	-	74	C	5			
	321	Object Oriented Programming	EDA505 RC,RD								2	-	1	1	152	E	8			
	322	Entrepreneurial education	EDLC317								2	-	1	-	78	C	5			
	Total hours per week, total assessments per semester and total credits per semester for mandatory discipline (MD) and elective discipline (ED)				16	1	9	1	402	4E 2C	30	16	0	9	2	338	4E 2C	30		
				27			402	OVP 0A/ R	26			338	OVP 1A/ R							

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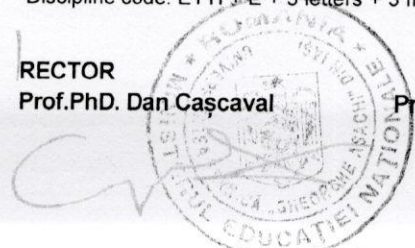
x = credits and verifications not included in the total.

Discipline code: ETTI + E + 3-letters + 3 figures +α, where α = missing / letter = common for the faculty/section (E – English).

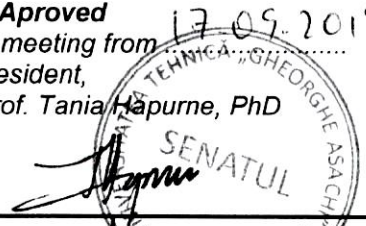
RECTOR  
 Prof.PhD. Dan Cașcaval

DEAN  
 Prof.PhD.Daniela Tărniceriu

HEAD OF DEPARTMENT  
 Assoc. Prof. PhD. Luminița Scripcariu



**Approved**  
 In Senat meeting from 17.09.2019  
 Senat President,  
 Assoc.Prof. Tania Hapurne, PhD



**CURRICULUM**

**4th Year 2019 - 2020**

No.	Name of the discipline	Disc. Code ETTI__	Condi- tionali- ties	1st Semester						2nd Semester							
				No. hrs/week./discipline					Fin Eval	K	No. hrs/week./discipline					Fin Eval	K
				C	S	L	P	SI			C	S	L	P	SI		
MD	401	Microcontrollers	EDIS401T	2	-	2	-	48	E	4							
	402	Mobile Communications	EDIS402T	3	-	1	1	34	E	4							
	403	Digital Communications	EDIS404T	3	1	1	-	34	E	4							
	404	Electronic Technology	EDIS406T	2	-	1	1	48	C	4							
	405	Radiocommunications	EDIS405T	3	-	1	1	60	E	5							
	406	Computer Networks and Operating Systems	EDID403T								3	-	2	-	86	C	6
	407	Communication Skills	EDIC408								1	-	-	-	12	VP	1
	408	Management	EDIC412								2	-	1	-	36	C	3
ED	409	1. Optical Communications	EDOS409T	3	-	1	-	74	C	5							
		2. Introduction to Nanoelectronics	EDOS410T														
	410	1. High Frequency Design Techniques	EDOS411T	2	-	1	-	62	E	4							
		2. Microwave Devices and Circuits for Radiocommunications	EDOS412T														
	411	1. Noise in Integrated Structures	EDOS413T								2	-	1	1	74	C	5
		2. Electromagnetic Compatibility	EDOS414T														
	412	1. Statistical Data Processing	EDOS415T								3	-	2	-	86	E	6
		2. Advanced Signal Processing Methods	EDOS416T														
413	1. Telephone Systems	EDOS417T								3	-	2	-	86	E	6	
	2. Internet-of-Things	EDOS418T															
IA	414	Elaboration of Bachelor Diploma Project	EDIS412											3s x 30ore/s=90 ore	-	A/R	3
	415	Defense of Bachelor Diploma Project	EDIS413											-	-	260	E
FD	416	Analog VLSI Design Techniques	EDIS404M	4	-	-	2	72	E	6							
	417	Digital VLSI Design Techniques	EDIS406M								3	-	-	2	60	E	5
	418	Fundamentals of Data Acquisition Systems	EDID403E								3	-	2	-	60	E	5
	419	Integrated Structures for Specific Applications 2	EDLS425	2	-	2	-	74	C	5							
	420	Functional Verification of Digital Integrated Circuits	EDIA602 TMP	2	-	2	-	74	E	5							
	421	Electroacustics	EDIS404E								2	-	2	-	48	C	4
	422	Automotive Electronics	EDIS408E	3	-	2	-	60	E	5							
	423	Peripheral Equipments and Human-Machine Interface	EDIS410E	2	-	2	-	74	E	5							
	424	Digital Audio Video Systems	EDID406E								2	-	2	-	48	VP	4
	425	Entrepreneurial education	EDLC426	1	-	2	-	78	C	5							
426	Applied entrepreneurial education	EDLC426								2	-	-	1	30	C	3	
Total hours per week, total assessments per semester and total credits per semester for mandatory discipline (MD) and elective discipline (ED)				18	1	8	3	360	5E 2C 0VP 0A/R	30	14	0	8	1	640	2E 3C 1VP 1A/R	30

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**RECTOR**  
 Prof.univ.dr.ing.Dan Cașcaval



**DEAN**  
 Prof.univ.dr.ing.Daniela Tărniceriu



**HEAD OF DEPARTMENT**  
 Conf.dr.ing.Luminița Scripcariu

